

# GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)  
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

## Office of the Controller of Examinations

### III B.Tech - I Semester (R-14) REGULAR PRACTICAL EXAMINATIONS, NOVEMBER 2017

#### TIME TABLE

Date: 26-10-2017

<b>Branch - Electronics and Communication Engineering</b>				
<b>Date</b>	<b>Name of the Lab</b>	<b>TIMINGS</b>		
		<b>9.15- 12.15</b>	<b>11.15 - 2.15</b>	<b>1.15 - 4.15</b>
<b>06-11-2017</b> (Monday)	Microprocessors and Interfacing Lab	<b>Section - C</b> (15481A04C3 - 4E8)	<b>Section - C</b> (15481A04E9 - 4H2)	<b>Section - C</b> (15481A04H3 - 4I5, 16485A0425 - 436)
<b>07-11-2017</b> (Tuesday)		<b>Section - D</b> (15481A04I6 - 4K7)	<b>Section - D</b> (15481A04K8 - 4N0)	<b>Section - D</b> (15481A04N1 - 4O0, 16485A0437 - 448, 14481A0498, 15485A0404)
<b>08-11-2017</b> (Wednesday)		<b>Section - B</b> (15481A0462 - 486)	<b>Section - B</b> (15481A0487 - 4B1)	<b>Section - B</b> (15481A04B2 - 4C2 & 16485A0413 - 423, 14481A04K3)
<b>09-11-2017</b> (Thursday)		<b>Section - A</b> (15481A0401 - 424)	<b>Section - A</b> (15481A0425 - 448)	<b>Section - A</b> (15481A0449 - 461 & 16485A0401 - 412, 13481A04C7)
<b>Date</b>	<b>Name of the Lab</b>	<b>9.00- 12.00</b>	<b>11.00 - 2.00</b>	<b>1.00 - 4.00</b>
<b>06-11-2017</b> (Monday)	Pulse & Digital Circuits Lab	<b>Section - A</b> (15481A0401 - 425)	<b>Section - A</b> (15481A0426 - 449)	<b>Section - A</b> (15481A0450 - 461 & 16485A0401 - 412, 13481A04C7)
<b>07-11-2017</b> (Tuesday)		<b>Section - C</b> (15481A04C3 - 4E9)	<b>Section - C</b> (15481A04F0 - 4H4)	<b>Section - C</b> (15481A04H5 - 4I5, 16485A0425 - 436)
<b>08-11-2017</b> (Wednesday)		<b>Section - D</b> (15481A04I6 - 4L0)	<b>Section - D</b> (15481A04L1 - 4N6)	<b>Section - D</b> (15481A04N8 - 4O0, 16485A0437 - 448, 14481A0498, 15485A0404)
<b>09-11-2017</b> (Thursday)		<b>Section - B</b> (15481A0462 - 486)	<b>Section - B</b> (15481A0487 - 4B1)	<b>Section - B</b> (15481A04B2 - 4C2 & 16485A0413 - 423, 14481A04K3)

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#### TIME TABLE

Date: 26-10-2017

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Date	Name of the Lab	TIMINGS		
		9.00- 12.00	11.00 - 2.00	1.00 - 4.00
06-11-2017 (Monday)	Digital Communications Lab	<b>Section - D</b> (15481A04I6 - 4L0)	<b>Section - D</b> (15481A04L1 - 4N6)	<b>Section - D</b> (15481A04N8 - 4O0, 16485A0437 - 448, 14481A0498, 15485A0404)
07-11-2017 (Tuesday)		<b>Section - B</b> (15481A0462 - 486)	<b>Section - B</b> (15481A0487 - 4B0)	<b>Section - B</b> (15481A04B1 - 4C2 & 16485A0413 - 423, 14481A04K3)
08-11-2017 (Wednesday)		<b>Section - A</b> (15481A0401 - 424)	<b>Section - A</b> (15481A0425 - 448)	<b>Section - A</b> (15481A0449 - 461 & 16485A0401 - 412, 13481A04C7)
09-11-2017 (Thursday)		<b>Section - C</b> (15481A04C3 - 4E9)	<b>Section - C</b> (15481A04F0 - 4H3)	<b>Section - C</b> (15481A04H4 - 4I5, 16485A0425 - 436)

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