

GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Office of the Controller of Examinations

II B.Tech - II Semester (R-14) Supplementary Practical Examinations, January 2017

TIME TABLE

Dt:28-12-2016

| Branch - Electrical and Electronics Engineering | | |
|--|---|---|
| DATE | NAME OF THE LAB | TIMINGS |
| | | 9.30 a.m. - 12.30 p.m. |
| 2-01-2017 (Monday) | Semiconductor Devices & Circuits Lab | 13481A0215, 232, 14481A0269, 15485A0240, 251 |
| 3-01-2017 (Tuesday) | Electrical Machines Lab - I | 13481A0232, 14481A0280, 282, 2A0 |
| Branch - Electronics and Communication Engineering | | |
| DATE | NAME OF THE LAB | TIMINGS |
| | | 9.30 a.m. - 12.30 p.m. |
| 2-01-2017 (Monday) | Analog Communications Lab | 14481A0414, 427, 431, 435, 452, 4J7 |
| 4-01-2017 (Wednesday) | Analog Circuits Lab | 14481A04E9, 4I2, 4J9, 4K3, 4M5, 15485A0455 |
| Branch - Computer Science and Engineering | | |
| DATE | NAME OF THE LAB | TIMINGS |
| | | 9.30 a.m. - 12.30 p.m. |
| 31-12-2016 (Saturday) | Operating Systems and Compiler Design Lab | 14481A0594, 5E7, 15485A0529 |
| 2-01-2017 (Monday) | Advanced Data Structures Lab | 14481A0530, 594 |
| Branch - Information Technology | | |
| DATE | NAME OF THE LAB | TIMINGS |
| | | 9.30 a.m. - 12.30 p.m. |
| 2-01-2017 (Monday) | Employability Skills | 13481A1221 |


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