



Jawaharlal Nehru Technological University Kakinada
Kakinada – 533003, Andhra Pradesh

Recognizes

Department of Electronics and Communication Engineering
Gudlalleru Engineering College, Gudlalleru

As

RESEARCH CENTRE

For the

Academic Years 2021-22 & 2022-23

Kakinada
19th March, 2022




REGISTRAR
REGISTRAR
J.N.T. University Kakinada
Kakinada-533003



Jawaharlal Nehru Technological University, Kakinada
Kakinada – 533 003, Andhra Pradesh

recognizes

Department of Electronics and Communication Engineering
Gudlavalleru Engineering College
Seshadri Rao Knowledge Village
Gudlavalleru-521356

as a research centre

for the academic years
2018-19 & 2019-20

Kakinada
18th July, 2019

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Registrar
REGISTRAR
J.N.T. University Kakinada
Kakinada-533003



Jawaharlal Nehru Technological University, Kakinada
Kakinada – 533 003, Andhra Pradesh

recognizes

Department of Electronics and Communication Engineering
Gudlavalleru Engineering College, Gudlavalleru

as a research centre

for the academic years
2016-17 & 2017-18

Kakinada
6th February, 2017

C. S. Reddy

Registrar
REGISTRAR
J.N.T. University Kakinada
KAKINADA-533 003

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

Department of Electronics and Communication Engineering

Details of Available Equipment, Software at Research Center

S. No.	Equipment / Software	Cost in Rs. (lakhs)
1	Microwind 3.9 Package – 35 Users	7.00
2	Cadence University Standard Bundle -AICTE-RPS (2022-23)	9.32
3	NetSim Academic Version v13.1 (2022-23)	6.04
4	iThenticate Software	5.51
5	Acer i5 Processor (8 GB RAM and 1TB HDD,4GB RAM and 1TB HDD, 2GB RAM and 320 GB HDD (40 computers)	17.80
6	Spectrum Analyser, Digital Storage Oscillator	2.44
7	ARM 926 Kit	3.50
8	MULTISIM-2001,11.0, 14.0 & NI Ultiboard 14.0	12.64
9	Code Composer Studio,Quartus-II-3.0	3.04
10	XILINX 6.2i/6.3i,7.1i,13.1,4.7,Vivado 2016.2 Modelsim SE 10.1b	11.25
11	MATLAB 2007B,7.4, 9.22008a,R2013b), R2017a	17.25
12	Mentor Graphics Higher Education Programme(HEP1,HEP2), Pyxix 10.2, Pyxix 10.3	13.16
13	NI LabVIEW 2018	10.50
14	ANSYS Academic Teaching HFSS	6.75
15	Microsoft Campus Agreement Renewal	2.58

Faculty working as Supervisor and Co-Supervisor with Research Scholars and Area

S.No.	Name of the Supervisor/ Co-Supervisor	Title of the Ph.D work	Name of the research scholar and organization	Registered with	Status
1	Dr. M. Kamaraju, Supervisor	Implementation and Analysis of Emotion Recognition System For Speech Signals	Mr.B.Rajasekhar Gudlalleru Engineering College	JNTUA, Ananthapura mu	Awarded
2	Dr. M. Kamaraju, Co-Supervisor	Low Power VLSI	P.Indira, GITAM University	CU Shah university	Awarded
3	Dr. M. Kamaraju, Co-Supervisor	Dynamic Power Optimization and Modeling of Logic architecture for FPGA	Mr. V. Prasanth, Pragathi Engineering College, Surampalem	JNTUK, Kakinada,	Pursuing
4	Dr. M. Kamaraju, Supervisor	Memory design using FINFET technology	Mrs. T. Venkatalakshmi, Gudlalleru Engineering College	JNTUK, Kakinada	Pursuing
5	Dr. M. Kamaraju, Supervisor	Design and Analysis of computational intelligence based architecture for real time environment	Mr. N. Sambamurthy, Gudlalleru Engineering College	JNTUK, Kakinada	Pursuing
6	Dr. M. Kamaraju, Supervisor	Performance evaluation of digital filters using algorithmic noise tolerant architecture for low power DSP applications	Mr. P. Bujjibabu, Aditya Engineering College, Kakinada	JNTUK, Kakinada	Pursuing
7	Dr. M. Kamaraju, Co-Supervisor	Design of Low Power SoC based portable device for virtually impaired	Mr.Y. Sri Chakrapani, Gudlalleru Engineering College	JNTUK, Kakinada	Pursuing
8	Dr. M. Kamaraju, Co-Supervisor	Design and Performance issues of 0.18 μ m CMOS OTA in biomedical applications(ECG)	Mr. B. R. B. Jaswanth, Gudlalleru Engineering College	JNTUK, Kakinada	Thesis Submitted
9	Dr. M. Kamaraju, Supervisor	Design and Analysis of Power Optimized Hybrid Embedded Controller Core Architecture	Mrs. T. Subhashini, Gudlalleru Engineering College	JNTUK, Kakinada	Pursuing
10	Dr. M. Kamaraju, Supervisor	Design and Analysis of Sub threshold logic circuits using	Ms. Lakshmi Barla, Gayathri Women's	JNTUK, Kakinada	Pursuing

		Power optimization Approaches	Engineering College, Vizag		
11	Dr. M. Kamaraju, Co-Supervisor	VLSI Signal Processing	Mr. K. Yugandhar, SRKR Engineering College, Bhimavaram	Annamalai University	Pursuing
12	Dr. M. Kamaraju, Supervisor	Design and Analysis of Application Specific Integrated Processor Architecture using Lowpower VLSI Techniques	Mrs. V. Priyadarshini, Gudlavalleru Engineering College, Gudlavalleru	JNTUK, Kakinada	Pursuing
13	Dr. V. V. K. D. V. Prasad, Supervisor	Wireless Communications	Mr. Ch. Rambabu, Gudlavalleru Engineering College	JNTUK, Kakinada	Pursuing
14	Dr. V. V. K. D. V. Prasad, Supervisor	Image Processing	Mr. M. V. Srikanth,	JNTUK, Kakinada	Pursuing
15	Dr. V. V. K. D. V. Prasad, Supervisor	Design and Performance issues of 0.18 μ m CMOS OTA in biomedical applications(ECG)	Mr. B. R. B. Jaswanth, Gudlavalleru Engineering College	JNTUK, Kakinada	Thesis Submitted
16	Dr. V. V. K. D. V. Prasad, Co-Supervisor	Low Power SRAM Design	Mr. M. Damodhar Rao Gudlavalleru Engineering College	JNTUK, Kakinada	Pursuing
17	Dr. V. V. K. D. V. Prasad, Co-Supervisor	Low power and Area Optimized CMOS VLSI Design pertaining to Analog and Digital Applications	Mr.V.S. Nagaraju JNTU Hyderabad	JNTUK, Kakinada	Pursuing
18	Dr. V. V. K. D. V. Prasad, Supervisor	Adaptive Filters for Estimation of Signals in Noisy Environemt	Mrs. B. Naga Sirisha, Gudlavalleru Engineering College	JNTUK, Kakinada	Pursuing
19	Dr. V. V. K. D. V. Prasad, Supervisor	Satellite Image Segmentation for Disaster management and Rescue planning	Mr. M. Nagu, VKR & VNB Engineering College, Gudivada	JNTUK, Kakinada	Pursuing

20	Dr. V. V. K. D. V. Prasad, Supervisor	Image Processing	Mr. P. Rama Krishna Gudlavalleru Engineering College, Gudlavalleru	JNTUK, Kakinada	Pursuing
21	Dr. V. V. K. D. V. Prasad, Supervisor	Image Segmentation using Optimization techniques application to Bio medical images	Mr. A. Srinivas KITS, Warangal	JNTUK, Kakinada	Pursuing
22	Dr. V. V. K. D. V. Prasad, Co-Supervisor	Image Processing	Mr. Murali Krishna A RVR & JC College of Engineering, Guntur	Annamalai University	Pursuing
23	Dr. K.N. Prakash Supervisor	Medical Image Processing	Mr.S.Srinivas Reddy	KL University	Completed
24	Dr. K.N. Prakash Supervisor	Development of a Technique for assessment and detection of Liveness face and Spoof face in Biometrics	Mr. B. Suresh Amrutha Sai Institute of Engineering & Technology, VJA	JNTUK, Kakinada	Pursuing
25	Dr. K.N. Prakash Supervisor	New Feature Descriptors for content based Medical Image Retrieval	Mrs. D. Naga Devi Vasavi Collge of Engineering, Hyderabad	JNUK, Kakinada	Pursuing
26	Dr. Ch. Balaswamy Supervisor	An Efficient implementation of localization algorithms for wireless sensor networks	J.Ravichander SR Engineering College, Warangal	JNTUA, Anapapur	Awarded
27	Dr. Ch. Balaswamy Supervisor	Security attacks in mobile Ad hoc Networks	V. Jai Kumar QIS College of Engineering & Technology, Ongole	JNTUA, Anapapur	Pursuing
28	Dr. Ch. Balaswamy Supervisor	Life time enhancement routing algorithms for mobile Adhoc Networks	K.C.K. Naik QIS College of Engineering & Technology, Ongole	JNTUA, Anapapur	Thesis Submitted
29	Dr. Ch. Balaswamy Supervisor	Performance Analysis of Attacks and Counter Measures in Wireless Sensor	Mr.P. Venkata Rao LBR College of Engineering, Mylavaram	JNTUK, Kakinada	Thesis Submitted

		Networks using NS 2			
30	Dr. Ch. Balaswamy Supervisor	Security System in wireless Communication Network	Ms. K. Amruthavalli VRS & YRN College of Engineering & Technology, Chirala	JNTUK, Kakinada	Pursuing
31	Dr. Y. Rama Krishna Supervisor	Mobile Antennas for 5G Applications	Mr. B. Ajay Kumar Gudlalleru Engineering College, Gudlalleru	JNTUK, Kakinada	Pursuing