

# GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)  
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

## Office of the Controller of Examinations

### III B.Tech - II Semester (R17) Regular Practical Examinations, November 2020

#### TIME TABLE

Dt: 23-11-2020

Branch - Electronics and Communication Engineering				
Date	Name of the Lab	TIMINGS		
		9.30 am- 12.30 pm	11.00 am- 02.00 pm	01.00 pm - 4.00 pm
<b>28-11-2020</b> (Saturday)	Microprocessor and Microcontroller Interfacing Lab (EC2515)	Section - A (17481A0401 - 425)	Section - A (17481A0426 - 450)	Section - A (17481A0451 - 457 & 18485A0401 - 416)
<b>30-11-2020</b> (Monday)		Section - B (17481A0458 - 481)	Section - B (17481A0482 - 4A5)	Section - B (17481A04A6 - 4B6 & 18485A0417 - 429)
<b>01-12-2020</b> (Tuesday)		Section - D (17481A04H9 - 4K4)	Section - D (17481A04K5 - 4M5)	Section - D (17481A04M6 - 4N4 & 18485A0441 - 455)
<b>02-12-2020</b> (Wednesday)		Section - C (17481A04B7 - 4E0)	Section - C (17481A04E1 - 4G3)	Section - C (17481A04G4 - 4H8 & 18485A0430 - 440)
<b>01-12-2020</b> (Tuesday)	Linear Integrated Circuits Applications Lab (EC2545)	15481A0459	-	-
Date	Name of the Lab	9.30 am- 12.30 pm		01.00 pm - 4.00 pm
<b>28-11-2020</b> (Saturday)	Digital Signal Processing Lab (EC2552)	Section - D (17481A04H9 - 4L7)		Section - D (17481A04L8 - 4N4 & 18485A0441 - 455, 15481A0459,16481A04M9, 4N6)
<b>30-11-2020</b> (Monday)		Section - A (17481A0401 - 435)		Section - A (17481A0436 - 457 & 18485A0401 - 416)
<b>01-12-2020</b> (Tuesday)		Section - C (17481A04B7 - 4F2)		Section - C (17481A04F3 - 4H8 & 18485A0430 - 440)
<b>02-12-2020</b> (Wednesday)		Section - B (17481A0458 - 493)		Section - B (17481A0494 - 4B6 & 18485A0417 - 429)

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#### TIME TABLE

Dt: 23-11-2020

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Branch - Electronics and Communication Engineering				
Date	Name of the Lab	TIMINGS		
		9.30 am- 12.30 pm	11.30 am- 02.30 pm	01.30 pm - 4.30 pm
28-11-2020 (Saturday)	VLSI Lab (EC2553)	Section - C (17481A04B7 - 4E0)	Section - C (17481A04E1 - 4G3)	Section - C (17481A04G4 - 4H8 & 18485A0430 - 440)
30-11-2020 (Monday)		Section - D (17481A04H9 - 4K5)	Section - D (17481A04K6 - 4M9)	Section - D (17481A04N0 - 4N4 & 18485A0441 - 455, 15481A0459, 16481A04M9, 4N6)
01-12-2020 (Tuesday)		Section - B (17481A0458 - 481)	Section - B (17481A0482 - 4A5)	Section - B (17481A04A6 - 4B6 & 18485A0417 - 429)
02-12-2020 (Wednesday)		Section - A (17481A0401 - 425)	Section - A (17481A0426 - 450)	Section - A (17481A0451 - 457 & 18485A0401 - 416)

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Principal

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### III B.Tech - II Semester (R17) Regular Practical Examinations, November 2020

#### TIME TABLE

Date: 23-11-2020

Branch - Computer Science and Engineering			
Date	Name of the Lab	TIMINGS	
		9.30 am - 12.30 pm	1.30 pm - 4.30 pm
28-11-2020 (Saturday)	Data Mining Lab (CS2514)	Section - A (17481A0501 - 555 & 18485A0501 - 509)	Section - B (17481A0556 - 5B6 & 18485A0510 - 515)
30-11-2020 (Monday)		Section - C (17481A05B7 - 5H2 & 18485A0516 - 526)	Section - D (17481A05H3 - 5N2 & 16481A05D5, 5I8, 17485A0506)
Date	Name of the Lab	9.30 am - 12.30 pm	1.30 pm - 4.30 pm
28-11-2020 (Saturday)	Mini Project (CS2515)	Section - C (17481A05B7 - 5H2 & 18485A0516 - 526)	Section - D (17481A05H3 - 5N2 & 16481A05D5, 5I8, 17485A0506)
30-11-2020 (Monday)		Section - A (17481A0501 - 555 & 18485A0501 - 509)	Section - B (17481A0556 - 5B6 & 18485A0510 - 515)

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**III B.Tech II Semester (R17) Regular Practical Examinations, November 2020**

**TIME TABLE**

**Date: 23-11-2020**

<b>Branch - Information Technology</b>			
<b>DATE</b>	<b>NAME OF THE LAB</b>	<b>TIMINGS</b>	
		<b>9.00 am - 12.00 Noon</b>	<b>1.20 pm - 4.20 pm</b>
<b>28-11-2020</b> (Saturday)	Computer Networks and Data Mining Lab (IT2513)	Section - A (17481A1201 - 1251)	Section - B (17481A1252 - 12B2)

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