

# SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)  
Seshadri Rao Knowledge Village, Gudlavalleru

Examination Section

## II B.Tech II Semester (R17) Regular Examinations, April 2025 (Readmitted Students)

### Time Table

Timings: 10:00 a.m. to 1.00 p.m.

22<sup>nd</sup> April 2025

DATE & DAY	ECE
03.05.2025 (Saturday)	Digital Circuit Design (EC2505)
06.05.2025 (Tuesday)	Transmission Lines and Waveguides (EC2528)
08.05.2025 (Thursday)	Analog Circuits (EC2529)
13.05.2025 (Tuesday)	Analog Communications (EC2530)
15.05.2025 (Thursday)	Fundamentals of Data Structures (CS2501)
17.05.2025 (Saturday)	<b>Open Elective - I</b>
	Elements of Civil Engineering (CE2515)

**Note:** Seating arrangement will be notified on the day of each examination half an hour before the commencement of the examination.

  
Controller of Examinations

  
Principal

Copy to: HOD – ECE/Exam Section  
Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)