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Naga Chambers, 3ª Floor, D.No. 12-1-16, Plot No. 49, Survey No. 1051, Waltair Road,Opp, HDFC Bank, Visatsiapatnam, Andhra Pracest, 330002 Plot No. 47 & 48, Flat No. 305, Sat Stimivas a Reasonary shafi Nagar. Old Sanig Malkajgiri, Hyderabad, Telengana State = 500.056 Gudlavalleru Engineering College Seshadri Rao Knowledge Village

Gudlavalleru - 521 356, Krishna District. A.P.

and the stand of the stand way at the self of All cheques / Demand Drafts/ Crossed and Account Payee only) are to be payable to KONSOLS. Goods sold once will not be taken back. Interest @ 2% per month will be charged on all overdue Received in good condition as For KONSOLS 2. per order and as described in 3, this invoice. All disputes are subject to Visakhapatnam jurisdiction. Warranty on all equipment lies with manufacturer and shall be directly provided by manufacturer Warranty Vold If tampered or damaged. 4 5. 6. Our responsibility ceases when goods have been handed over to Carrier / Carriers representative 7. **Receiver Sign** Authorised Sig

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Naga Chambers, 3rd Floor, D.No. 12-1-16, Plot No. 49, Survey No. 1051, Waltair, Main, Road, Opp. HDFC Bank, Visaknapainam, Andhra Padesh - 530002 Plot No. 47 & 48, Flat No. 305, Sai Srinivasa, Kesidency, Shah Nagar, Old Safilguda, Malkajgiri, Hyderabad, Telengana State - 500 056 Guolavailerto - Hyderabad, Telengana State - 500 056

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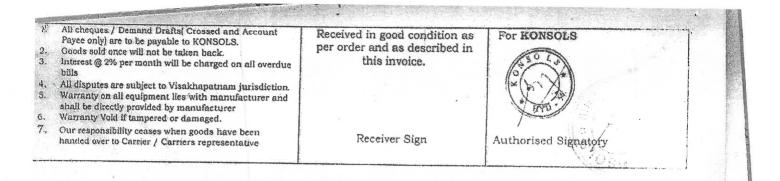
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Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District. A.P.

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Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District. A.P.

1 -NAME : KONSOLS : 4855135000000572 : KVB/ MVP COLONY BRANCH A/C NO BANK NAME Receiver Sign IFSC CODE : KVEL0004855 Authorised Signatory



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Naga Chambers, 3rd Floor, D.No. 12-1-16, Plot No. 49, Survey No. 1051, Waltair Main Road Opp, HDFC Bank, Visakhapatnam, Andhra Prodesn – 53006.2 Plot No. 47 & 48, Flat No. 305, Sai Srinivasa Residency, Shafi Nagari Old Safiiguda, Malkajgiri, Hyderabad, Tclengana, Sciele 500 Grin SeShadri Kao Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District, A.P.

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Naga Chambers, 3rd Floor, D.No. 12-1-16, Plot No. 49, Starvey No. 1051, Waltair Main Road,Opp. HDFC Bank, Visakhapatnam, Andhra Pradesh - 530002 Plot No. 47 & 48, Flat No. 305, Sai Srinivasa Residency, Chañ Nagar, Old Safilguda, Malkajgiri, Hyderabad, Telengana State - 500 056

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	OLS BANK DETAILS	Received in good condition as	For KONSOLS
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NAME A/C NO BANK NAME IFSC CODE	: KONSOLS : 485513500000572 : KVB/ MVP COLONY BRANCH : KVBL0004855	Receiver Sign	Authorised Signatory





Naga Chambers, 3rd Floor, D.No. 12-1-16, Plot No. 49, Survey No. 1051, Waltair Main Road,Opp. HDFC Bank, Visakhapatnam, Andhra Pradesh - 530002 Plot No. 47 & 48, Flat No. 305, Sai Srinivasa Residency, Saef Magar, Old Safilguda, Malkajgiri, Hyderabad, Telengana State - 500 056

Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District. A.P.

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Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District, A.P.

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PRINCIPAL Seshadri Rao Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District, A.P.

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PRINCIPAL Seshadri Rao Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District. A.P.

Tax Invoice

Bees Software Solutions Pvt Ltd. Villa No 1, H.No.5-45/A/1, Gangasthan Dulapally (V), Quthbullapur(M), MEDCHAL (D), Telangana State-50001	4	8	oice No ivery N			Dated 15-Apr-2019 Mode/Terms of Payment 365 Days		
GSTIN/UIN: 36AAGCB4484L1Z9 State Name : Telangana, Code : 36 CIN: U72900AP2014PTC094871 E-Mail : admin@beessoftware.in			oplier's ES/GEC/	Ref. LETTER/(2/19-20		s ference(s)	
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PRINCIPAL Seshadri Rao Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District. A.P.

Tax Invoice

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Seshadri Rao Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District. A.P.

Tax Invoice

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PRINCIPAL

Seshadri Rao Gudlavalleru Engineering College Seshadri Rao Knowledge Village Gudlavalleru - 521 356, Krishna District, A.P.

2.3.2: ICT TOOLS

- Online Microsoft APPs for Teaching and Learning Methodologies
- Online Class Conduction Mode Guidelines
- Online Class Recordings/Materials
- Online Test/Assignments
- Virtual Labs
- Online Lab Conduction
- Online Faculty/Student Development Programs/Webinars
- Online Internship / Project Evaluation
- Online NPTEL Courses

Microsoft APPs for Teaching and Learning Methodologies



Word

Bring out your best writing.



Excel

Discover and connect to data, model and analyze it, and visualize insights.



Class Notebook

Organize your lesson plans in a digital notebook, and create a workspace for students.



Forms

Create surveys, quizzes, and polls and easily see results in real time.



Kaizala

A simple and secure mobile chat app for work



OneDrive Store, access, and share your files in one place.



PowerPoint Design professional presentations.



Outlook Business- class email through a rich and familiar Outlook experience.



Sway Create and share interactive reports, presentations, and personal stories.



Teams

The customizable, chat-based team workspace in Office 365.



Stream

Share videos of classes, meetings, presentations, and training sessions.

Online Class Conduction Mode Guidelines

- 1. Faculty has to initiate the class 10 min before the scheduled time to set the required settings and to facilitate the students to join.
- 2. Students may be permitted to join at any time without any time restrictions(by pass lobby)
- 3. Attendance may be given to all the students who joined the class
- 4. Download the attendance after all the students left the class before end the meeting and save it in a folder with the name <subject name>-<period>-<date> Ex. CD-2nd period-29/08/2020
- 5. Faculty should fill the online Microsoft form and also mark the attendance in the hard copy.
- 6. Review the recorded class, edit if required and then only make it available to the students
- 7. Mark the attendance in hard copy and handover the same to HoD if it was completely filled up
- 8. Adopt any engagement strategies to improve student engagement
- 9. Ask questions about the topic at regular intervals so that the students will be more attentive in the class.
- 10. If any student creates disturbance by unmuting the microphone or sending personal messages in chat box, the student should be warned and may be removed from the meeting.
- 11. Conduct at least one period per week as tutorial by assigning online quiz/assignment with few questions and analyze answers as a part of discussion.
- 12. For lab related subjects play recoded videos/conduct laboratory experiments using virtual labs/use any other online platforms to practice laboratory experiments for individual or combined sections.
- 13. Conduct online quiz after online demo for lab experiments
- 14. Conduct at least 2 online quizzes (Maximum of 10 MCQs each) and 1 or 2 assignments for every unit of the subject before CT-I to be scheduled shortly.

Steps to be followed for online classes

- 1. Login to Microsoft Teams, click on teams
- 2. Changing the channel preferences :
 - select your class team
 - click general channel
 - click ...
 - click on *manage channel*
 - select Any one can post messages

3. Starting the class :

- click general
- click *meet now* on video camera symbol
- click join

4. Changing the meeting Permissions

- click on people
- click on ... more options
- select manage permissions
- Set Always let callers bypass the lobby is "ON"
- select against who can present "only me"
- click save.

5. Recording the class :

- select more options by clicking on ...
- click start record when class is started
- Before the end of the class, click on stop recording

6. Downloading Attendance Sheet :

- After all the students left, click on people
- click on ... more options
- click Download attendance

7. Ending the Meeting

• Check whether all students left the class or not. If any student is not left, remove him from meeting.

- select more options by clicking on ...
- click End meeting

8. Changing the channel preferences :

- select your class team
- click general channel
- click ...
- click on manage channel
- select Only owners can post messages

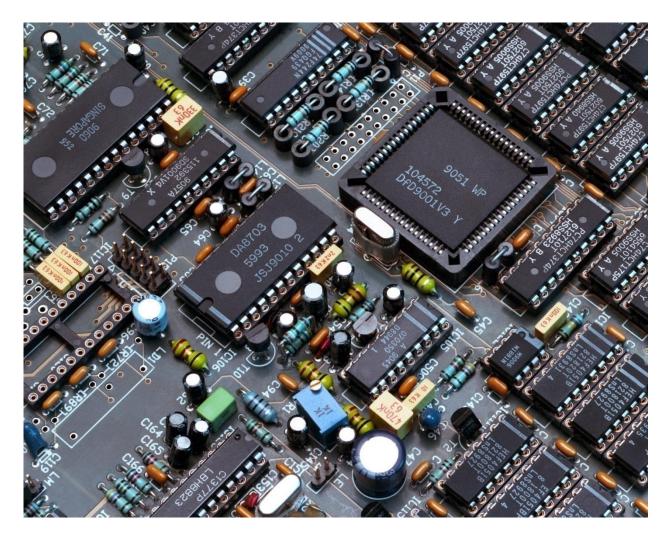
9. Uploading the Video to Microsoft Streams :

- Download the recorded video from the channel
- Open Microsoft Streams
- Click on Create
- Click on Upload Video
- Drag Video files or browse to upload
- Change the Title for Video
- Click on Permissions, select private
- Click on Publish

ONLINE CLASS RECORDINGS/MATERIALS

Online Link

https://sway.office.com/S8eNvb92Os6AirTu?ref=Link&loc=play



Electronics & Communication Engineering

Dr. M.Kamaraju

Subject Name: EmbeddedSystem DesignClass Note BooSubject Name: Principles of VLSI DesignClass Note Boo

Class Note BookClass VideoClass Note BookClass Video

ONLINE TEST MODEL QUESTION PAPER

PVLSID Class Test-1 (PVLSID-EC2539- R17-2018-22-III-I-ECE-D)

3

CMOS processing steps can be divided into two parts , in those Transistors are formed in ______ phase , while wires are built in the ______ phase. * (2 Points)

- BEOL (Back End of Line), FEOL(Front End of Line)
- FEOL (Front End of Line), BEOL (Back End of Line)
- both Czochralski phase
- none of the above

4

Input impedance of MOSFET is _____. * (2 Points)

- less than FET but more than BJT
- more than FET and BJT
- more than FET but less than BJT
- less than FET and BJT
 - O βp/βn < 1 ----- LO-skew inverter</p>
 - $^{\circ}$ All of the above

In p -well process , NMOS Transistors are built in _____ and the PMOS Transistors is placed in the _____ substrate. * (2 Points)

- p-well, n-type
- n-well , p-type
- p-well, p-type
- n-well , n-type

6

Which of the following is a rapid process? * (1 Point)

- Wet Oxidation
- Dry Oxidation
- $^{\bigcirc}$ Atomic layer deposition
- Cryogenic Oxidation

7

The common p-type dopant in silicon is/are * (1 Point)

- Boron
- Boron and Gallium
- Gallium

nMOS fabrication process is carried out in * (1 Point)

- $^{\circ}$ thin wafer of a single crystal
- $^{\bigcirc}$ thin wafer of multiple crystals
- $^{\circ}$ thick wafer of a single crystal
- thick wafer of multiple crystals

9

What is the value of Vin, when both transistors of CMOS inverter are in saturation region? * (1 Point)

>VDD/2

0

○ =VDD/2

O VDD

10

When Vgs>Vt and Vds>(Vgs-Vt), the transistor operate in ______ region. * (1 Point)

- active
- cut-off
- saturation

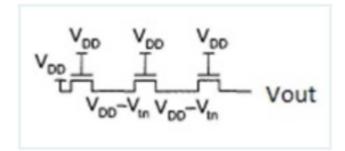
In CMOS circuits, the pull up device is _____. * (1 Point)

- NMOS transistor
- PMOS transistor
- RL
- BJT

12

Which of the following is true in the case of a positive photoresist? * (2 Points)

- O Positive photo resist provides higher resolution than negative resist but is less sensitive to light
- Negative photo resist provides higher resolution than positive resist but is less sensitive to light
- O Positive photo resist provides higher resolution than negative resist but is more sensitive to light
- Negative photo resist provides higher resolution than positive resist but is more sensitive to light



What is the value of the voltage Vout in the diagram shown? * (2 Points)

- O VDD
- VDD Vtn
- VDD 2Vtn
- VDD 3Vtn

14

4. CMOS devices uses _____. * (1 Point)

- Bipolar transistors
- Complementary E-MOSFETs
- $^{\circ}$ Class A operation
- D-MOS devices

In a MOSFET, drain current starts only when VGS is _____. * (1 Point)

- $^{\bigcirc}$ less than Vth
- negative
- zero
- $^{\bigcirc}~$ greater than Vth

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📲 Microsoft Forms

Online Demonstration and Video Link of Laboratories Handled

Sl. No.	Year and Semester	Name of Laboratory	Online Demonstration and
	Semester		Video Link
1	II Year and I Semester	Electronic Devices Lab	https://gecgudlavallerumic- my.sharepoint.com/:v:/g/personal/ravisec e_gecgudlavallerumic_in/EYLH5I7XJtxMlp pvLWweviMB9Wov_59LmJ60LOz2L2m7yA ?e=qxlQmx
2	II Year and II Semester	Analog Circuits Lab	<u>https://gecgudlavallerumic-</u> my.sharepoint.com/:v:/g/personal/ravisec e_gecgudlavallerumic_in/EUUt9V8uTVhJr DX-Q7cpXc4BKLpZ-EofgStcDABWQlykbw
3	Semester	Digital Circuit Design Lab	https://web.microsoftstream.com/video/b 52847fb-ae3e-42f0-bc85-7d3c62fde7cc
4	III Year	Analog and Digital Communications Lab	https://www.onenote.com/edu/classnote book/manage/1-9c417130-ed07-4acc- bfd7-3e5bb71be8a5
5	and I Semester	SemesterLinear Integrated Circuitsmy.sharepoint.com/personal/Applications Labcgudlavallerumic_in/DocumerNotebooks/LICA%20Lab%20-R	https://gecgudlavallerumic- my.sharepoint.com/personal/malli797_ge cgudlavallerumic_in/Documents/Class%20 Notebooks/LICA%20Lab%20-R17-2020-21- III-I-ECE
6	III Year	Digital Signal Processing Lab	https://gecgudlavallerumic.sharepoint.co m/:f:/s/DSPLAB-EC2552-R17-2018-22-III-II- ECE- B/Egd7AeyuFpFMnOtQXRa91h4Bg7x0OwL QGqU9Psmjr3cbTQ?e=ZPg6b3
7	and II Semester	VLSI Lab	https://gecgudlavallerumic- my.sharepoint.com/:v:/g/personal/nagasir ishab_gecgudlavallerumic_in/Ec0BxQy224 NLqiTipOtcP2oBwU2Q_luQ7_0SoyWfREUi Tg?e=reemMQ
8	IV Year and I Semester	Microwave and Optical Communications Lab	https://teams.microsoft.com/l/team/19%3aeb66 ca86d50d4cf585fc7253e8ab56be%40thread.tac v2/conversations?groupId=058b7490-e15e- 43fc-aa4b-3a6a74f19f4a&tenantId=df7217c9- cec8-4cc2-8e6a-a1cfbfadb321

VIRTUAL LABS

The COVID-19 pandemic has challenged the status quo of engineering education, especially in highly interactive, hands-on design classes. Traditionally, these classes are held in maker spaces and fabrication labs and therefore, can be profoundly impacted by the lack of an innovative design studio which facilitates communication and a collaborative campus environment. Our intention is to demonstrate that engineering design courses can be successfully held remotely. We provide an example of how we effectively adjusted an intensive hands-on, group project-based engineering course. In 2019, teaching staff at colleges had to adapt their curricula to go fully remote when campuses closed in response to the growing COVID-19 pandemic. Under those plans, even if a class can successfully be taught remotely, instructors are expected to commute to campus in that case, taking public transit in an urban setting to deliver it in person while synchronously offering the same class as an online course.

What are virtual labs?

• Virtual labs are simulated learning environments that allow students to complete laboratory experiments online and explore concepts and theories without stepping into a physical science lab.

• Students can try out lab techniques for the first time and become more familiar with advanced lab equipment that might otherwise be inaccessible.

• Through animations, students can explore life science at a molecular level and look inside the machines they are operating.

• Virtual lab software creates opportunities for alternative access to science education.

URL: https://www.vlab.co.in/

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							Objectives						84	
		ł	1. To pro	vide remote-a	ccess to simulation-based La	bs in various disciplines	of Science and Engineering.							
		1	2. To enti	nuse students	to conduct experiments by a	rousing their curiosity. Th	his would help them in learning	basic and ad	lvanced concepts	through remote experimentation.			f	
					ete Learning Management Sy res, animated demonstrations		I Labs where the students/ tea	achers can av	vail the various to	ools for learning, including additional we	b-		y	
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In our college, all departments are gone through the above link identified the experiments which are related to curriculum and completed the laboratories. some of the labs are enclosed here

GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)Seshadri Rao Knowledge Village, Gudlavalleru - 521 356.

DIGITAL CIRCUIT DESIGN Lab Academic Year: 2019-2020

Class: II	I B.Tech-II Sem (A-Sec)	 Faculty: 1) Mrs.T. Venkata Lakshmi 2) Mr.Ch.Rambabu 3) Mrs.S.Bhavani 4) Mr.P.Anil Kumar 5) Mrs. M.Deepika Krishna z
S.No.	Experiment Name	Link
1.	Analysis and Synthesis of Boolean	http://vlabs.iitkgp.ernet.in/dec/exp3/index.html
1.	Expressions using Basic Logic Gates	

No. of Experiments Conducted: ELECTRONIC DEVICES LAB

S. No.	Name Of The Experiment	Link
	I-V Characteristics of Junction Diode	http://vlabs.iitkgp.ernet.in/be/exp5/index.html
1	and Breakdown Characteristics of Zener Diode	http://vlabs.iitkgp.ernet.in/be/exp10/index.html
2	LED and Photo Diode I-V Characteristics	Not available
3	DC Power Supply (design, simulation, hardware implementation and testing)	Not available
4	Zener voltage regulator (design, simulation, hardware implementation and testing)	http://vlabs.iitkgp.ernet.in/be/exp10/index.html
5	Diode Clipper	http://vlabs.iitkgp.ac.in/psac/exp1/index.html#
6	Diode Clamper	Not available
7	MOSFET Characteristics	Not available
8	BJT Characteristics	http://vlabs.iitkgp.ernet.in/be/exp11/index.html
9	Transistor Switch	Not available
10	BJT Voltage – Divider bias circuit with stabilization	Not available
11	MOSFET Voltage-Divider Bias circuit (Design, simulation, implementation and testing)	Not available

• With this interest, from Gudlavalleru Engineering College started to develop VLABS in association with IITR. The institution has approached them through mail and got permission from IITR as nodal center in 2020. The letter is attached below.

	भारतीय प्रौधोगिकी संस्थान रूड़की TEL : +91-1332-285590 (0) विधुत अभियान्त्रिकी विभाग रूड़की 247667, उत्तराखण्ड, भारत
North Contraction	Indian Institute of Technology Roorkee Department of Electrical Engineering Roorkee -247667, (UTTARAKHAND), INDIA
प्रो. आर. एस. आनंत	7 4,
Prof. R.S. Anand	
	Dated: August 07, 203
The Principa	
Gudlavalleru	Engineering College
	o Knowledge Village, Gudlavalleru-521 356
Krishna Dist	rict, A.P. Sub: Virtual Labs Nodal Centre
college as a Noda Nodal Coordinate	your Expression of Interest for Virtual Labs, it gives me immense pleasure to designate yo I Centre for Virtual Labs. As nominated by you, Dr. Y. Syamala has been accepted to act as to r from your college. This approval is valid up to <u>31st December 2020</u> . Subject to the followin ions and any subsequent directives as issued by Ministry of Education from time to time:
1. Approved stat	tus of AICTE/STEB/UGC is mandatory for your college.
2. The necessar	y infrastructure (dedicated space having personal computers with 1Mbps broadband intern
connectivity)	to be maintained at your own cost for Virtual Labs.
3. Nodal centers	will get operational technical as well as financial support as given below.
4. Nodal Coordi	nator will get an honorarium of Rs.5000/- at the end of semester after submission of Virtual la
usage report f	rom the Institute, duly signed by the Director/Principal of the Institute.
5. Each Nodal C	Centers will organize at least one workshop per semester as an outreach activity of Virtual Labs
6. Students are r	to be charged any extra fee for providing Virtual Labs facility for their usage.
7. Nodal Coordi	nator should attend the meeting held at IIT Roorkee as per schedule and a semester-wise repo
of Virtual lab	s usages and feedback by the faculty members and students should be submitted.
8. Strict adheren	ce to the standard lab procedures and cyber security laws need to be followed.
9. Any violation	of the above will result in automatic cancellation of Nodal Centre status for your college without
giving any no	tice.
above. We thank you aga	edge the receipt of this letter and the acceptance of the Terms & Conditions mention in for your interest in the Virtual Labs project and appreciate your endeavor in the service of t
student communit Thank You, Sincerely,	y. Wish you all the best!
Sincerery,	
A	18 2020
1000	
(Dr. R.S. Anand) Principal Institu	

Under this Nodal center from GEC submitted the proposals to IITR for the development of VLABS from various departments. The proposals are accepted from Civil Engineering Solid Mechanics laboratory, Optical Communication laboratory from Electronics Communication Engineering, Data Mining laboratory from Computer Science Engineering department.

• Before initializing the development the institution has encouraged the faculty to conductworkshop on Virtual Labs in association with IITR in the month of August 2020.

The program schedule is attached here.