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INTELLECTUAL PROPERTY RIGHTS



INTELLECTUAL PROPERTY RIGHTS & PATENTS – A VIEW

HOME INTELLECTUAL PROPERTY RIGHTS & PATENTS – A VIEW

Intellectual Property Rights & Patents – A View

Intellectual Property
Rights & Patents
– A View

NAME OF FDP/TRAINING PROGRAMME**PERIOD/DATE**

Intellectual Property Rights & Patents – A View

10-08-2020 To 12-08-2020

RESOURCE PERSONS

Mr.M.Vijaya Kumar,Patent And Trademark Attorney,Hyderabad. And Others

INTERNAL BENEFITED

234

EXTERNAL BENEFITED**TOTAL BENEFITED**

12

236

OUTCOME OF THE EVENT

Filling Procedure Of Patents International And Drafting Of Specification

SPONSORING AUTHORITY

AICTE

Resource Persons

- 1.Mr.M.Vijaya Kumar,Patent and Trademark Attorney,Hyderabad.
- 2.Mr.M.Srinivas,President M/S Educational Societyfor IPR,Hyderabad
- 3.Mr.O.Prasada Rao, Deputy Controller of Patents and Designs, Chennai

- 1.Overview of IPR,Patent Filing in India & International, Drafting of specifications
 2. Overview of Intellectual Property Rights – Importance to Academia.
 - 3.Intellectual Property Rights & Patents –A View
- Outcome: Filling Procedure of Patents International and Drafting of Specification

POPULAR EVENTS

Intellectual Property
Rights & Patents
– A View

(faculty_details/Intellectual-
Property-Rights-Patents-A-
View)

Intellectual Property Rights & Patents – A View (Faculty_details/Intellectual-Property-Rights-Patents-A- View)

BY (FACULTY_DETAILS/INTELLECTUAL-PROPERTY-RIGHTS-PATENTS-
A-VIEW)

OPENING HOURS

Mon - Fri :

9.00 am - 6.00 pm

Saturday :

9.00 am - 5.00 pm

Sun : Closed

QUICK LINKS

[Infrastructure \(Infrastructure_labs/Electronic-Devices-And-Circuits-Lab\)](#)

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SR Gudlavalleru Engineering College



BLOCK CHAIN ARCHITECTURE DESIGN AND USE CASES

HOME BLOCK CHAIN ARCHITECTURE DESIGN AND USE CASES

Block Chain Architecture Design and Use Cases

Block Chain
Architecture Design
and Use Cases

NAME OF FDP/TRAINING PROGRAMME		PERIOD/DATE	
Block Chain Architecture Design And Use Cases		10-08-2020 To 15-08-2020	
RESOURCE PERSONS		INTERNAL BENEFITED	
Dr.Sugata Gangopadhyay IIT,Associate Professor, Roorkee And Others		22	
EXTERNAL BENEFITED	TOTAL BENEFITED	OUTCOME OF THE EVENT	
78	100	SPONSORING AUTHORITY	
		AICTE	

Resource Persons:

- 1.Dr.Sugata Gangopadhyay IIT,Associate Professor, Roorkee
- 2.Dr.Raju Halder, Associate Professor, IIT Patna
- 3.Dr. Modi Chirag Navin Chandra, Associate Professor, NIT Goa
- 4.Sri.Revanth Kumar, CEO Unturtle Technologies Hyderabad
- 5.Prasanth Sahoo, Blockchain Expert, TCS Ltd, 6.Bipul kumar Shahi, Blockchain Expert

Outcome :

1. Hashing and Crptography with Block Chain Implementation.
- 2.Building a Decentralised Application Framework from Scrath with MEAN Stack
- 3.Overview of blockchain technology and its working principles in detail were discussed by considering Bitcoin as a case study.
- 4.Complete hands on Programming session to build a decentralized application framework from scratch and deploy it using MEAN stack.
- 5.Complete awareness session on Industry applications of block chain technology.

POPULAR EVENTS

Block Chain
Architecture Design
and Use Cases

**Block Chain Architecture Design And Use Cases
(Faculty_details/Block-Chain-Architecture-Design-And-Use-
Cases)**

(faculty_details/Block-
Chain-Architecture-

BY (FACULTY_DETAILS/BLOCK-CHAIN-ARCHITECTURE-DESIGN-AND-USE-
CASES)

Design-and-Use-
Cases)

OPENING HOURS

Mon - Fri : 9.00 am - 6.00 pm

Saturday : 9.00 am - 5.00 pm

Sun : Closed

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BLOCKCHAIN ARCHITECTURE DESIGN AND USE CASES (DECENTRALIZED NETWORK SYNCHRONIZATION)

 HOME

- BLOCKCHAIN ARCHITECTURE DESIGN AND USE CASES (DECENTRALIZED NETWORK SYNCHRONIZATION)

Blockchain Architecture Design and Use Cases (Decentralized Network Synchronization)

NAME OF FDP/TRAINING PROGRAMME

Blockchain Architecture Design And Use Cases (Decentralized Network Synchronization)

PERIOD/DATE

17-08-2020 To 22-08-2020

RESOURCE PERSONS

Dr.Sugata Gangopadhyay IIT,Associate Professor, Roorkee And Others

INTERNAL BENEFITED

19

EXTERNAL BENEFITED

81

TOTAL BENEFITED

100

OUTCOME OF THE EVENT**SPONSORING AUTHORITY**

AICTE

Resource Persons:

- 1.Dr.Sugata Gangopadhyay IIT,Associate Professor, Roorkee
- 2.Dr.Raju Halder, Associate Professor, IIT Patna
- 3.Dr. Modi Chirag Navin Chandra, Associate Professor, NIT Goa
- 4.Sri.Revanth Kumar, CEO Unturtle Technologies Hyderabad
- 5.Prasanth Sahoo, Blockchain Expert, TCS Ltd, 6.Bipul kumar Shahi, Blockchain Expert

Outcome:

- 1.Role of Smart Contracts and the potential applications of blockchain technology.
- 2.Hands on Programming session for building a decentralzed application framework from scratch and deploy it using MEAN stack.
- 3.Research opportunities in the field of Smart Contracts.
- 4.Hashing and Cryptography with Block chain Implementation.
- 5.Awareness session on Smart contracts

POPULAR EVENTS

(faculty_details/Blockchain-
Architecture-Design-and-

**Blockchain Architecture Design And Use Cases
(Decentralized Network Synchronization)
(Faculty_details/Blockchain-Architecture-
Design-And-Use-Cases-Decentralized-Network-
Synchronization-)**

Use-Cases-Decentralized-
Network-Synchronization-)

BY (FACULTY_DETAILS/BLOCKCHAIN-ARCHITECTURE-DESIGN-
AND-USE-CASES-DECENTRALIZED-NETWORK-SYNCHRONIZATION-)

OPENING HOURS

Mon - Fri : 9.00 am - 6.00 pm

Saturday : 9.00 am - 5.00 pm

Sun : Closed

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BLOCKCHAIN ARCHITECTURE DESIGN AND USE CASES (SMART CONTRACTS WITH ETHEREUM AND SOLIDITY)

 HOME

- BLOCKCHAIN ARCHITECTURE DESIGN AND USE CASES (SMART CONTRACTS WITH ETHEREUM AND SOLIDITY)

Blockchain Architecture Design and Use Cases (Smart Contracts with ethereum and Solidity)

NAME OF FDP/TRAINING PROGRAMME

Blockchain Architecture Design And Use Cases(Smart Contracts With Ethereum And Solidity)

PERIOD/DATE	RESOURCE PERSONS	
24-08-2020 To 29-08-2020	Dr.Raju Halder, Associate Professor, IIT Patna And Others	
INTERNAL BENEFITED	EXTERNAL BENEFITED	TOTAL BENEFITED
21	79	100
OUTCOME OF THE EVENT	SPONSORING AUTHORITY	
	AICTE	

Resource Persons

- 1.Dr.Sugata Gangopadhyay IIT,Associate Professor, Roorkee
- 2.Dr.Raju Halder, Associate Professor, IIT Patna
- 3.Dr. Modi Chirag Navin Chandra, Associate Professor, NIT Goa
- 4.Sri.Revanth Kumar, CEO Unturtle Technologies Hyderabad
- 5.Prasanth Sahoo, Blockchain Expert, TCS Ltd,
- 6.Bipul kumar Shahi, Blockchain Expert

Outcome:

- 1.Ethereum, a blockchain based smart contract platform.
- 2.Supply chain management as a blockchain application
- 3.Hands -on Programming session using Solidity Language

POPULAR EVENTS

(faculty_details/Blockchain-Architecture-Design-and-Use-Cases-Smart-Contracts-with-ethereum-and-Solidity-)

Blockchain Architecture Design And Use Cases(Smart Contracts With Ethereum And Solidity) (Faculty_details/Blockchain-Architecture-Design-And-Use-Cases-Smart-Contracts-With-Ethereum-And-Solidity-)

🔗 BY (FACULTY_DETAILS/BLOCKCHAIN-ARCHITECTURE-DESIGN-AND-USE-CASES-SMART-CONTRACTS-WITH-ETHEREUM-AND-SOLIDITY-)

OPENING HOURS

Mon - Fri : 9.00 am - 6.00 pm

Saturday : 9.00 am - 5.00 pm

Sun : Closed

QUICK LINKS

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DESIGN AND DEVELOPMENT OF VLSI CIRCUITS USING MENTOR GRAPHICS TOOLS

HOME DESIGN AND DEVELOPMENT OF VLSI CIRCUITS USING MENTOR GRAPHICS TOOLS

Design and Development of VLSI Circuits using Mentor Graphics Tools

Design and Development
of VLSI Circuits using
Mentor Graphics Tools

NAME OF FDP/TRAINING PROGRAMME**PERIOD/DATE**

Design And Development Of VLSI Circuits Using Mentor Graphics Tools

14-12-2020 To 15-12-2020

RESOURCE PERSONS

Mr.K.Pradeep,Sr.Application Engineer In Apply Volt

INTERNAL BENEFITED

41

EXTERNAL BENEFITED

0

TOTAL BENEFITED

41

OUTCOME OF THE EVENT**SPONSORING AUTHORITY**

Mr.K.Pradeep,Sr.Application Engineer In Apply Volt

Resource Person:

Mr.K.Pradeep,Sr.Application Engineer in Apply Volt

Outcome:

Participants can design and develop digital circuits using Mentor graphics HEP1 tools

Theory Session on “Basics of ASIC Design and ASIC Vs FPGAs,Full Custom Flow and Semi Custom Design Flow, Latest Advancements in VLSI industry” and lab session on “pyxis schematic and ELDO Simulator”.

Lab Session on “RTL to GDS Flow” which includes reading Verilog Netlist in pyxis Layout, Floor –planning placement & Routing(PR),Physical Verification with Calibre(DRC,LVS,PEX).

15th December 2020:

Lab Session on “Cell Based Design” which includes Floor –planning placement & Routing(PR),Physical Verification using industry standard Calibre tool (DRC,LVS,PEX).

Layout creation from schematic and analyzed parameters like power dissipation, delay etc.,. then there was a demonstration session on process of post layout simulation , RC extraction(XRC) and physical verification step using calibration tool(LVS).

POPULAR EVENTS

Design and Development
of VLSI Circuits using
Mentor Graphics Tools

Design And Development Of VLSI Circuits Using Mentor Graphics Tools (Faculty_details/Design-And-Development-Of-VLSI-Circuits-Using-Mentor-Graphics-Tools)

(faculty_details/Design-
and-Development-of-
VLSI-Circuits-using-
BY (FACULTY_DETAILS/DESIGN-AND-DEVELOPMENT-OF-VLSI-CIRCUITS-USING-MENTOR-GRAPHICS-TOOLS)

Mentor-Graphics-
Tools)

OPENING HOURS

Mon - Fri : 9.00 am - 6.00 pm

Saturday : 9.00 am - 5.00 pm

Sun : Closed

QUICK LINKS

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SRGUDLAVALLERU ENGINEERING COLLEGE



RECENT TRENDS IN ELECTRIC VEHICLES-PHASE I

 HOME ○ RECENT TRENDS IN ELECTRIC VEHICLES-PHASE I

Recent trends in Electric vehicles-Phase I

About GEC

The Gudlavalluru Engineering College was established in 1998 by A.A.N.M. & V.V.R.S.R. Educational Society, Gudlavalluru. The College is situated at Gudlavalluru village, surrounded by lush green fields providing serene atmosphere to the learners. The college offers B.Tech programme in six branches of Engineering, viz., Civil, EEE, ME, ECE, CSE & IT, and PG courses in MBA and M.Tech (SE), M.Tech (PEED), M.Tech (MD), M.Tech (VLSI&ES), and M.Tech (CSE). The College is offering two new UG courses in Artificial Intelligence & Data Science and Internet of Things with an intake of 60 from the academic year 2020-21 onwards. All the eligible UG engineering programmes have been accredited by National Board of Accreditation (NBA) of AICTE, New Delhi and by NAAC. The college is also Accredited by TCS.

About the Department

The Department of Mechanical Engineering was established in 1998 and is offering B.Tech course in Mechanical Engineering and M.Tech in Machine Design. The department has qualified and experienced staff and well equipped laboratories with the state-of-the-art facilities. It has been accredited 4 times (one time under Tier-1) by the National Board of Accreditation (NBA) and recognized as Research Centre by JNTUK, Kakinada. Students of the department have added value to the department by getting placed in reputed core and software companies and by excelling in competitive examinations. The Department is marching forward keeping pace with the technological advances in Mechanical Engineering.

About the FDP

The main aim of the online faculty development program is to introduce advances in academic research as well as industry practices in development of solar energy driven electric vehicles, role of hybrid energy storage systems for EVs applications, energy sources hybridization and future of Electric Vehicles. Lectures from eminent speakers from premier institutions and industries will be delivered and will help to know about the recent trends in electric vehicles.

Chief Patrons

Dr. Nageswara Rao Vallurupalli, *Chairman*
Sri Srinivasa Rao Musunuri, *Co-Chairman*
Er. Satyanarayana Rao Vallurupalli, *Secretary & Correspondent*
Sri Ramakrishana Vallurupalli, *Co-Secretary & Correspondent*

Patron

Dr. G.V.S.N.R.V. Prasad, *Principal*

Co-Patrons

Dr. M. R. Ch. Sastry, *Vice Principal, Academics*
Dr. P. Kodanda Rama Rao, *Vice Principal, Administration*

Advisor

Dr. P. Ravindra Babu, *Advisor to the Management*
Dr. P. Nageswara Reddy, *Director (R&D, C&T, PG Studies)*
Dr. B. Karuna Kumar, *Director (AS&A)*

Convener

Dr. A. Jawahar Babu, *Professor & HoD, ME*

Coordinators

Dr. Narasimha Suri Tinnaluri, *Assistant Professor*
(Contact No: 9704838763)
Dr. Prabhakar Rao Ganji, *Associate Professor*
(Contact No: 8332877053)

Important Dates

Last Date for Registration : 14-03-2021
Confirmation through E-Mail : 15-03-2021
Dates of Programme : 17th -19th March 2021
(10.00 AM to 12.00 Noon)

Resource Persons:

- Dr. K. Srinivas Reddy , IIT Madras
- Dr. D. Raveendhra, Zunik Energies Pvt. Ltd., Somalkot
- Dr. A. Geetha, SRMSIT, Chennai

Registration link:

https://docs.google.com/forms/d/1YSI8xH3D385jxKYhovpKZCOSS8bEOITW_e7GmwKxMW9s/edit

Certificate:

E-certificate will be given to the registered participants who attend all the sessions.

No Registration Fee

The number of participants is limited to 150



Three Days Online Faculty Development Programme on Recent Trends in Electric Vehicles-PHASE I

17th - 19th March 2021



Organized by

Department of Mechanical Engineering
GUDLAVALLERU ENGINEERING COLLEGE
(An Autonomous Institute with permanent affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village
Gudlavalluru-521356
Krishna Dt. Andhra Pradesh, India.
Phone:08674-273737,273888
Fax:08674-273957
E-Mail:tnsuri@gmail.com
Website: www.gecgudlavalleu.ac.in

NAME OF FDP/TRAINING PROGRAMME

Three Day Online FDP On

PERIOD/DATE

17 Mar 2021 To 19 Mar 2021

RESOURCE PERSONS

Dr. K Srinivasa Reddy, Dr. D. Ravindra, Dr. A, Geeta

INTERNAL BENEFITED

30

EXTERNAL BENEFITED

110

TOTAL BENEFITED

140

OUTCOME OF THE EVENT

Got Exposure On Electric Vehicles

SPONSORING AUTHORITY

Gudlavalluru Engineering College, Gudlavalluru

This FDP gives awareness on Electric vehicles.

POPULAR EVENTS



Recent Trends In Electric Vehicles-Phase I (Faculty_details/Recent-Trends-In-Electric-Vehicles-Phase-I)

BY (FACULTY_DETAILS/RECENT-TRENDS-IN-ELECTRIC-VEHICLES-PHASE-I)

(faculty_details/Recent-trends-in-Electric-vehicles-Phase-I)

OPENING HOURS

Mon – Fri : 9.00 am – 6.00 pm

Saturday : 9.00 am – 5.00 pm

Sun : Closed

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[Student Activities \(Student_activities/MEA-ACTIVITIES\)](#)

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